

Teradyne Catalyst Tester Manual

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Memory - Teradyne

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1997 - Teradyne introduces Catalyst, the first System-On-A-Chip (SOC) test system. 1998 - Teradyne introduces the Integra J750, a test solution for high volume test of low-cost devices. 2000 - Teradyne Japan Division announces a new generation of image sensor test systems, the IP-750.

Teradyne - Wikipedia

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Teradyne ' s Production Board Test TestStation family of in-circuit test systems share a common software environment and are configurable from a range of a few test points for basic shorts detection up to more than 15,000 test points for more complex PCBAs Page 10/25 Download ...

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